

Table of Contents

VHDL Design Tool Flow for Portable FPGA Implementation <i>Vijaykumar Guddad, Alexandra Kourfali, and Dirk Stroobandt</i>	1
Implementation of an FPGA - Raspberry Pi SPI Connection <i>Haissam Hajjar and Hussein Mourad</i>	7
Accelerating FPGA-Placement With a Gradient Descent Based Algorithm <i>Timm Bostelmann, Tobias Thiemann, and Sergei Sawitzki</i>	13
60 GHz Low-Noise Amplifier in a 70 nm GaAs m-HEMT Technology for Multi-band Impulse Detection System <i>Pape Sanoussy Diao, Thierry Alves, Benoit Poussot, and Martine Villegas</i>	19
A Convolutional Neural Network Accelerator for Power-Efficient Real-Time Vision Processing <i>Junghee Lee and Chrysostomos Nicopoulos</i>	25